

IN THE CLAIMS:

1. (Currently Amended) A semiconductor device comprising:
a capacitance element one end of which is connected to a power supply;
a first comparator which has two input nodes having opposite polarity to each other and receives a reference voltage and ~~a power supply voltage~~ an output of the other end of the capacitance element at their inputs to compare the respective voltage values to output a signal indicating a comparison result;

a first resistor element which connects the one side input node and the other side input node of the first comparator; and

~~a capacitance element one end of which is connected to a power supply terminal which applies said power supply voltage and the other end of which is connected to the one side input node of the comparator~~

wherein the first comparator activates the output signal indicating the comparison result when the voltage difference between the inputted reference voltage and the inputted output of the other end of the capacitance element occurs ~~power supply voltage varies~~.

2. (Currently Amended) A semiconductor device as defined in claim 1, wherein:

the first comparator is a hysteresis comparator which activates the output signal indicating the comparison result when the voltage difference between the inputted reference voltage and the ~~power supply voltage~~ inputted output of the other end of the capacitance element becomes larger than a predetermined hysteresis width.

3. (Currently Amended) A semiconductor device as defined in claim 1, wherein further comprising:

a second and third resistor elements connected in series between the power supply ~~terminal~~ voltage and the ground terminal to divide the power supply voltage;

a second comparator having two input nodes and receives the ~~power supply voltage~~ voltage divided by the second and the third resistor elements and the reference voltage at its inputs to compare those; and

a logic OR circuit which takes a logic OR operation of the output signal of the first comparator and the output signal of the second comparator.

4. (Currently Amended) A semiconductor device as defined in claim 3 ~~any of the claims 1-3~~, wherein further comprising:

a reset portion which received the output signals of the first comparator or the logic OR circuit at their inputs, and stops the operation of the system including the semiconductor device when the output signal of the first comparator or the output signal of the second comparator is activated.

5. (Currently Amended) A semiconductor device as defined in claim 3 ~~any of the claims 1-3~~, wherein further comprising:

a switching part which switches the value of the ~~power supply voltage~~ output of the other end of the capacitance element which is inputted to either of the input nodes of the first comparator to an arbitrary value.

6. (Original) Semiconductor device as defined in claim 5, wherein further comprising:

a control section which operates the switching part at turning on the power of the semiconductor device.

7. (Currently Amended) A semiconductor device comprising:

first and second capacitance elements one end of each of which is connected to a power supply;

a ~~first and a second comparators each of~~ comparator which has two input nodes having opposite polarity to each other and ~~receives a receiving~~ reference voltage and a ~~power supply~~

voltage an output of the other end of the first capacitance element at their inputs to compare the respective voltage values to output a signal indicating a comparison result;

a second comparator which has two input nodes having opposite polarity to each other and receiving a reference voltage and an output of the other end of the second capacitance element at their inputs to compare the respective voltage values to output a signal indicating a comparison result;

a first and a second resistor elements each of which connects the one side input node and the other side input node of the first and the second comparators, respectively;

~~a first and a second capacitance elements, one end of which is both connected to a power supply terminal which applies said power supply voltage, and the other end of which is connected to the one side input node of the first and the second comparator, respectively;~~

a logic OR circuit which takes a logic OR operation of the output signal of the first comparator and the output signal of the second comparator; and

wherein the first and the second comparators respectively activate the output signal indicating the comparison results when the voltage difference between the inputted reference voltage and the ~~power supply voltage~~ varies inputted output of the other end of the capacitance element occurs, and the polarity of the input node which receives the ~~power supply voltage~~ output of the other end of the first capacitance element in the first comparator and the polarity of the input node which receives ~~the power supply voltage~~ an output of the other end of the second capacitance element in the second comparator are opposite to each other.

8. (Currently Amended) A semiconductor device as defined as defined in claim 7 wherein:

the first comparator and the second comparator respectively are hysteresis comparators which ~~activates~~ activate the output signal indicating the comparison result when the voltage difference between the inputted reference voltage and the ~~power supply voltage~~ inputted output of the other end of the first capacitance element is larger than a predetermined hysteresis width.

9. (Currently Amended) A semiconductor device as defined in claim 7, wherein further comprising:

a third and fourth resistor elements connected in series between the power supply terminal and the ground terminal to divide the power supply voltage; and

a third comparator which has two input nodes and compares the power supply voltage which is divided by a third and a fourth resistor elements and the reference voltage to output a signal indicating the comparison result to the logic OR circuit.

10. (Currently Amended) A semiconductor device as defined in ~~any of claims 7 to 9~~ claim 9, wherein:

there is further provided a reset part which receives the output signal of the logic OR circuit at its input and stops the operation of a system including the semiconductor device when the output signal of the first comparator, the second comparator, or the third comparator is activated.

11. (Currently Amended) A semiconductor device as defined in ~~any of claims 7 to 9~~ claim 9, wherein further comprising:

a switching part which switches the value of the ~~power supply voltage~~ output of the other end of the first capacitance element which is inputted to either of the input nodes of the ~~first and the second comparators~~ comparator and the value of the output of the other end of the second capacitance element which is inputted to either of the input nodes of the second comparator to an arbitrary value.

12. (Currently Amended) semiconductor device as defined in claim 11 ~~wherein~~ further comprising:

a control section which operates the switching part at turning on the power of the semiconductor device.